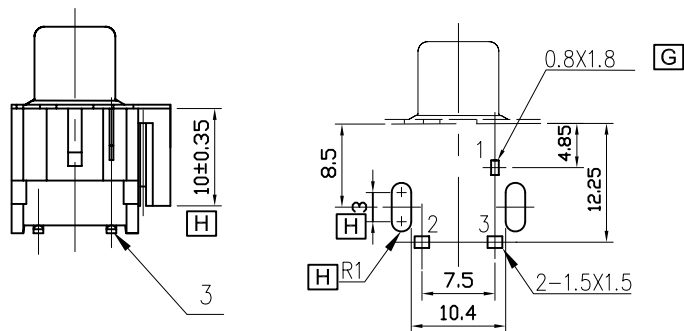
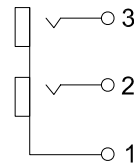


Dimensions of PC board holes(Commend)
Bottom View ⊙



G PCB TOLERANCE RECOMMEND ±0.05mm

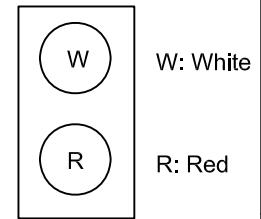
Circuit Diagram



B Order Information:

PJ14 2 1 -1001- 1- GF
SEE NOTES 3
COLOR LAYOUT
TOOLING SERIES
1 COLUMN
2 ROWS
RCA PITCH: 14.0

Color layout 1



C NOTES:

- ELECTRICAL:
A. DIELECTRIC STRENGTH: AC 500V FOR 1 MINUTE.
B. CONTACT RESISTANCE: 20 MILLIMOHS MAX..
C. INSULATION RESISTANCE: 100 MEGAOHMS MIN.
- RoHS COMPLIANT.

- E 3. FOR HIGH TEMPERATURE LEAD FREE MANUFACTURE PROCESS, ADD -GF SUFFIX.

CHART A E

PART NUMBER	MATERIAL
PJ1421-1001-1	ABS
PJ1421-1001-1-GF	PBT + 15% GLASS FILLED

- F 4. RECOMMENDED TEMPERATURE FOR SOLDER PROCESS:

A. MATERIAL: ABS

SOLDER CONDITION	TEMPERATURE
LEAD PROCESS	185 °C, 3 ~ 5 SECONDS
LEAD FREE PROCESS	185 °C, 3 ~ 5 SECONDS
HAND SOLDER PROCESS	185 °C, 3 ~ 5 SECONDS

B. MATERIAL: PBT + 15% GLASS FILLED

SOLDER CONDITION	TEMPERATURE
LEAD PROCESS	225 °C, 3 ~ 5 SECONDS
LEAD FREE PROCESS	225 °C, 3 ~ 5 SECONDS
HAND SOLDER PROCESS	225 °C, 3 ~ 5 SECONDS

H	ADD DIM "10±0.35"; REVISE PCB LAYOUT CORRECT BOM 5 & 6 QTY	Vegas	7/08/09'
G	CORRECT DIMENSIONS AND TOLERANCE	Vegas	4/24/09'
F	ADD NOTES 4	AXL	3/8/07'
E	ADD CHART A AND NOTES 3	AXL	2/19/07'
D	CHANGE MATERIAL WAS ABS	AXL	2/2/07'
C	ADD NOTES	AXL	9/25/06'
B	REVISED ORDER INFORMATION	AXL	7/11/06'
A	ISSUED	Kevin	06/28/06'
REV	ECN NO.	NAME	DATE

G

TOLERANCE	
LINEAR	ANGLES
< 6 ±0.2	.X'±
6~20±0.3	.X'±
>20±0.4	.XX'±
.XXX'±	



APPD: MAT'L:
CHKD: FINISH:
DRWN: Veags Q'TY:

TITLE:			
STACKED RCA PIN JACK			
PART NO.		PJ1421-1001-1	
DWG NO.		SC-20023	
UNITS	SCALE	SHEET	REV
MM	NONE	1 OF 1	H

No.	Name	QTY	Material	REMARK
6	Washer (White)	1	SEE CHART A	UL 94V-0
5	Washer (Red)	1	SEE CHART A	UL 94V-0
4	Base (Black)	1	SEE CHART A	UL 94V-0
3	Long terminal	1	BRASS H62 t=0.4	Tin 0.2um over CuSn 1-3um
2	Short terminal	1	BRASS H62 t=0.4	Tin 0.2um over CuSn 1-3um
1	Outer Conductor	1	Brass H65 t=0.4	Tin 0.2um over CuSn 1-3um